



09/990894

PATENT

COFE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein

Assignee: Matrix Semiconductor, Inc.

Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD
THEREFOR

Patent No.: 6,768,685 B1

Issued: July 27, 2004

Atty. Docket No.: 023-0013

September 30, 2004

ATTN: Certificate of Correction Branch
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT –
PTO MISTAKE (37 C.F.R. § 1.322(a))**

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

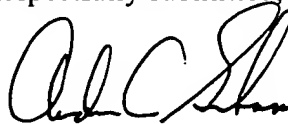
1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:
 - a) The Issue Fee Transmittal, Box 3, filed May 23, 2004.
 - b) Page 2, Reference AR, Form 1449 filed January 3, 2002.
 - c) Page 21, line 13 of the Application as filed on November 16, 2001.
 - d) Page 7, line 2 of the Response to Non-Final Office Action filed August 7, 2003, and page 26, claim 21, line 13 of the application as filed on November 16, 2001.
3. Please send the Certificate to:

Andrew C. Graham
ZAGORIN, O'BRIEN & GRAHAM, L.L.P.
7600B N. CAPITAL OF TEXAS HWY, SUITE 350
AUSTIN, TEXAS 78731-1191

Certificate
OCT 12 2004
of Correction

OCT 12 2004

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'A.C. Graham', written over the typed name.

Andrew C. Graham, Reg. No. 36,531

Attorney for Applicant(s)

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,768,685 B1

DATED : July 27, 2004

INVENTOR(S) : Roy E. Scheuerlein

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Cover Sheet, in the Assignee Section, "Mtrix" should read -- Matrix --.

On the Cover Sheet, in the Other Publications Section, "09/894,704" should read -- 09/897,704 --

Column 14, line 19, insert -- . -- after "required".

In claim 21, line 16, "circuit through" should read -- current through --.

MAILING ADDRESS OF SENDER:

ZAGORIN, O'BRIEN & GRAHAM, L.L.P.
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Austin, Texas 78731

PATENT NO. 6,768,685 B1

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